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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Anil K. Chinthakindi, et al. **Examiner:** Unassigned
Serial No: 10/710847 **Art Unit:** Unassigned
Filed: 08/06/04 **Docket:** BUR920040112US1 (17618)
For: FEOL/MEOL METAL RESISTOR FOR
HIGH END CMOS **Dated:** August 12, 2004

Commissioner for Patents
United States Patent Office
Alexandria, VA 23313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Publication No. 2003-338549, published on November 28, 2003.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450 on 8-13-04.

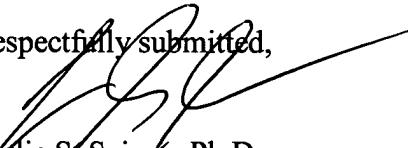
Dated: 8/13/04

*P. de Ruyge
P. de Langis*

Applicants are submitting copies of the above-cited reference.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,


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LSS/sf
Enclosure PTO Form 1449
One (1) reference

**LIST OF PRIOR ART
CITED BY APPLICANT**

(Use several sheets if necessary)



Atty. Docket No.
BUR920040112US1 (17618)

Serial No.

Applicant

Filing Date

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.